

10/032861

ABSTRACT OF THE DISCLOSURE

A circuit protector comprising a substrate, a conductive layer formed around the substrate, a narrowed portion formed on the conductive layer at a certain part, terminals formed at both ends of the substrate. The substrate has 5 1-30 % pores in a unit surface area in a vicinity of its surface. The present invention also relates to a mounting structure of the circuit protector onto a circuit board.

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